

High Performance PCI Bus, IEEE 488.2 Controller

PCI-GPIB

Registered
ISO 9001
Company

- IEEE 488.2 standard interface
- Talker/listener/controller
- Uses the new industry standard CB7210.2 chip
- 32 bit PCI Bus
- Data transfer rates over 1Mbytes/sec
- REP-INSW block transfer
- 1024 word FIFO buffer
- High speed state machine bus manager
- RF shielded IEEE-488 connector
- 7 Interrupt lines, shared interrupt capability
- Transparent interrupt enabling/disabling
- Includes complete GPIB-Library software support

The PCI-GPIB IEEE-488 interface converts any PCI bus PC into an instrumentation control and data acquisition system. Connect up to 14 instruments using standard IEEE-488 cables.

The PCI-GPIB is designed around the new industry standard CB7210.2 GPIB chip.

Greater than 1Mbyte/s transfer rates

The PCI-GPIB transfers data over the GPIB at rates in excess of 1 million bytes per second. A 1024 word FIFO buffer and the advanced REP-INSW ISR transfer method provide the highest possible rates.

Controlled by the high speed State Machine Bus Manager; the CB7210.2 and FIFO buffer are able to absorb all the information coming from the highest speed instruments. The high speed state machine provides byte-to-word packing and unpacking and since words carry twice the information bytes do, packed data requires fewer bus cycles to transfer the same GPIB information.

If you are worried that you may need more than 1MB/s, remember that Hewlett-Packard, the world's largest instrument manufacturer transfers data over the GPIB at no more than 800KB/s. The PCI-GPIB is more than fast enough for any application.



IEEE-488.2 (GPIB) compatibility

The PCI-GPIB adheres to ANSI/IEEE Standard 488-1978. Often referred to as the IEEE-488.2 bus, GPIB bus or HP-IB bus, the GPIB (General Purpose Interface Bus) is a standard for instrumentation communication and control for instruments from manufacturers the world over. The GPIB provides handshaking and interface communications over an 8 bit data bus employing 5 control and 3 handshake signals.

Equipped with a PCI-GPIB, a personal computer can control GPIB instruments, gather data from GPIB test equipment or become a data acquisition station in a GPIB system.

Plug & Play - No switches or jumpers

The PCI-GPIB is true Plug 'n' Play. Plug in the PCI-GPIB and run the installation software then start communicating. The Plug 'n' Play specification is a standard for system configuration of boards and

software which automatically configures the address and interrupt level of the PCI-GPIB. Note, the PCI-GPIB does not use DMA in favour of the superior REP-INSW high speed transfer.

The circuitry and software required for the Plug 'n' Play standard implements identification, configuration, conflict detection and resolution. The PCI-GPIB uses the industry standard AMCC 5933 PCI controller chip to implement Plug 'n' Play.

Windows 95/98, Windows 3.1 and DOS compatibility

The PCI-GPIB hardware supports popular operating systems and languages regardless of the operating systems support for Plug 'n' Play. The installation software will manage resources for you on non-Plug 'n' Play systems.

SPECIFICATION

The PCI-GPIB is compatible With IEEE-488.1 and IEEE-488.2 specifications.

Transfer rate	>1Mbyte/sec
Power	5VDC @ 375mA typ
Dimensions	134 X 97 millimetres
I/O connector	IEEE-488 standard 24 pin
Operating temp.	0-60°@10-90% rel. hum.
Storage temp.	-40 to 100°C @ 5-90% rel hum
Compliance	CE EMC.

DESCRIPTION	PRODUCT CODE
PCI-GPIB board	
1MB transfer	909 761 53
PCI-GPIB-300K board	
with 300KB transfer rate	960 039 53